

**CONVEYORIZED PLATING LINE AND METHOD
FOR ELECTROLYTICALLY METAL PLATING
A WORKPIECE**

ABSTRACT OF THE DISCLOSURE

To avoid voids in the metal layer in holes of printed circuit boards, a conveyORIZED plating line and a method for electrolytically metal plated printed circuit boards are proposed which provide measures for reducing an electric voltage that builds up between adjacent printed circuit boards being conveyed through the line.